

HIGH FREQUENCY SIGNAL POWER DETECTOR

ABSTRACT OF THE DISCLOSURE

5 A signal power detector includes an input coupling circuit, a rectifying operational amplifier, and a charge pump. The input coupling circuit is operably coupled to receive a signal and to convert the signal into a first input and a rectifying input. The rectifying operational amplifier is operably coupled to receive the first input and the rectifying input and to produce therefrom a rectified output signal that represents a peak
10 of the received signal. The charge pump converts the rectified output into a corresponding current, wherein the corresponding current represents power of the signal.